

_				rage i	Οţ
	ATTY. DKT	BWD:2353.005/34-001	SER. NO.	10/003,727	
	APPLICANT	Stapleton			
	FILING DATE	October 30, 2001	GROUP		

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
H. < ,	AA	5,352,986	October 4, 1994	Modgil et al	330/10	330/107
A. c.	AB	4,843,339	June 27, 1989	Burt et al	330/10	328/151
H.c.	AC	6,373,334 B1	April 16, 2002	Melanson	330/10	330/207
И. с.	AD	6,630,899 B2	October 7, 2003	Jayaraman	341/200	330/151
H. c.	AE	6,577,189 B2	June 10, 2003	Jayaraman etal	330/107	330/10
H, c ,	AF	5,777,512	July 7, 1998	Tripathi et al	330/207A	330/110
H-c.	AG	6,191,653	February 20, 2001	Camp,Jr.et al	330/129	330/10

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

H_ e .	АН	Dallago, Single-Cycle Quasi-Resibabt Converters as Sigma-Delta Audio Power Stages, IEEE June 1996, pp 645-650.		
H-c.	AI	Anderson, A NEW APPLICATION FOR ZERO-CURRENT-SWITCHED FULL-WAVE RESONANT CONVERTERS, European Power Electronics Ass. 1993, pp 83-86.		
H.c.	AJ	Luckjiff, Dobson, Divan, Interpolative Sigma Delta Modulators for High Frequency Power Electronic Applications, IEEE June 18, 1995, pp 444-449.		
H. c.	AK	Ballan, Declercq, 12V Σ-Δ AMPLIFIER IN 5V CMOS TECHNOLOGY, IEEE 1995 Custom Integrated Circuits Conference, pp 26.5.1-26.54		
H. e.	AL	Opris & Kovacs, A Video bandwidth Analog Median Circuit, IEEE 1995 Custom Integrated Circuits Conference, pp 26.4.1-26.4.4		
	Henry choe Date considered was of			
Rev. 10/94 (Form 3.05)				